PCI8735

User's Manual

Beijing ART Technology Co., Ltd.

Contents

Contents	1
Chapter 1 Overview	2
Chapter 2 Components Layout Diagram and a Brief Description	4
2.1 The Main Component Layout Diagram	4
2.2 The Function Description for the Main Component	4
2.2.1 Signal Input and Output Connectors	4
2.2.2 Potentiometer	4
2.2.3 Physical ID of DIP Switch	4
Chapter 3 Signal Connectors	6
3.1 The Definition of Signal Input Connectors	6
3.2 DI Definition of Signal Input and Output Connectors	6
3.3 DO Definition of Signal Input and Output Connectors	7
Chapter 4 Connection Ways for Each Signal	8
4.1 AD Single-ended Input Connection	8
4.2 AD Double-ended Input Mode	8
4.3 DIO Connection	9
Chapter 5 Notes, Calibration and Warranty Policy	10
5.1 Notes	10
5.2 AD Analog Signal Input Calibration	10
5.3 Warranty Policy	10
Products Rapid Installation and Self-check	12
Rapid Installation	12
Self-check	12
Delete Wrong Installation	12

Chapter 1 Overview

In the fields of Real-time Signal Processing, Digital Image Processing and others, high-speed and high-precision data acquisition modules are demanded. ART's PCI8735 data acquisition module, which brings in advantages of similar products that produced in China and other countries, is convenient for use, with high performance-price ratio.

ART's PCI8735 is a data acquisition module based on PCI bus. It can be directly inserted into IBM-PC/AT or a computer which is compatible with PCI8735 to constitute the laboratory, product quality testing center and systems for different areas of data acquisition, waveform analysis and processing. It may also constitute the monitoring system for industrial production process.

Unpacking Checklist

Check the shipping carton for any damage. If the shipping carton and contents are damaged, notify the local dealer or sales for a replacement. Retain the shipping carton and packing material for inspection by the dealer.

Check for the following items in the package. If there are any missing items, contact your local dealer or sales.

- PCI8735 Data Acquisition Board
- > ART Disk
 - a) user's manual (pdf)
 - b) drive
 - c) catalog
- Warranty Card

FEATURES

Analog Input

- ► Input Range: $\pm 10V$, $\pm 5V$, ± 2.5 , $0 \sim 10V$
- ➤ 12-bit resolution, the thirteen bit is sign bit
- ➤ Sampling Rate: up to 500KS/s
- ➤ Analog Input Mode: 32SE/16DI
- ➤ AD Conversion Time: ≤1.6uS
- Programmable Gain: 1, 2, 4, 8 (AD8251) or 1, 2, 5, 10 (AD8250) or 1, 10, 100, 1000 (AD8253)
- \triangleright Analog Input Impedance: 10MΩ
- ➤ Non-linear error: ±1LSB(Maximum)
- > System Measurement Accuracy: 0.1%
- ➤ Operating Temperature Range: 0°C~50°C
- ➤ Storage Temperature Range: -20°C~70°C

Digital Input

- ➤ Channel No.: 16-channel
- ➤ Electric Standard: TTL compatible
- ► High Level: $\ge 2V$
- ► Low Level: ≤ 0.8 V

Digital Output

➤ Channel No.: 16-channel

➤ Electrical Standard: TTL compatible

High Level: ≥2.4V Low Level: ≤0.5V

➤ Power-on Output: low level

Other features

On-board Clock Oscillator: 40MHz

Board Dimension: 131mm (L) * 81mm (W)*15mm (H)

Chapter 2 Components Layout Diagram and a Brief Description

2.1 The Main Components Layout Diagram



2.2 The Function Description of the Main Components

2.2.1 Signal Input and Output Connectors

CN1: analog signal input connectors

P1: on/off input port P2: on/off output port

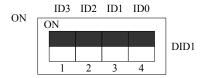
2.2.2 Potentiometer

RP1: AD analog signal input zero point adjustment potentiometer RP2: AD analog signal input full-scale adjustment potentiometer

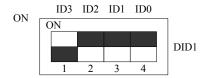
2.2.3 Physical ID of DIP Switch

DID1: Set physical ID number. When the PC is installed more than one PCI8735, you can use the DIP switch to set a physical ID number for each board, which makes it very convenient for users to distinguish and visit each board in the progress of the hardware configuration and software programming. The following four-place numbers are expressed by the binary system: When DIP switch points to "ON", that means "1", and when it points to the other side, that means "0." As they are shown in the following diagrams: place "ID3" is the high place. "ID0" is the low place, and the black part in the diagram represents the location of the switch. (Test software of the company often uses the logic ID management equipments and at this moment the physical ID DIP switch is invalid. If you want to use more than one kind of the equipments in one and the same system at the same time, please use the physical ID as much as possible. As for the differences between logic ID and physical ID, please refer to the function explanations of "CreateDevice" and

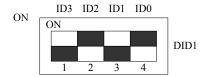
"CreateDeviceEx" of *The Prototype Explanation of Device Object Management Function* in *PCI8735S* software specification).



The above chart shows"1111", so it means that the physical ID is 15.



The above chart shows "0111", so it means that the physical ID is 7.



The above chart shows "0101", so it means that the physical ID is 5.

ID3	ID2	ID1	ID0	Physical ID (Hex)	Physical ID (Dec)
OFF (0)	OFF (0)	OFF (0)	OFF (0)	0	0
OFF (0)	OFF (0)	OFF (0)	ON (1)	1	1
OFF (0)	OFF (0)	ON (1)	OFF (0)	2	2
OFF (0)	OFF (0)	ON (1)	ON (1)	3	3
OFF (0)	ON (1)	OFF (0)	OFF (0)	4	4
OFF (0)	ON (1)	OFF (0)	ON (1)	5	5
OFF (0)	ON (1)	ON (1)	OFF (0)	6	6
OFF (0)	ON (1)	ON (1)	ON (1)	7	7
ON (1)	OFF (0)	OFF (0)	OFF (0)	8	8
ON (1)	OFF (0)	OFF (0)	ON (1)	9	9
ON (1)	OFF (0)	ON (1)	OFF (0)	A	10
ON (1)	OFF (0)	ON (1)	ON (1)	В	11
ON (1)	ON (1)	OFF (0)	OFF (0)	С	12
ON (1)	ON (1)	OFF (0)	ON (1)	D	13
ON (1)	ON (1)	ON (1)	OFF (0)	E	14
ON (1)	ON (1)	ON (1)	ON (1)	F	15

Chapter 3 Signal Connectors

3.1 The Definition of Signal Input Connectors

37-core plug on the CN1 pin definition

	/22	19	A 10
AII	37	18	AI2
A I 3	36	17	
A I 5	3.5		A I 4
A I 7	3.4	• 10	A 16
A 19	33	o 15	A 18
		14	AI10
AII1		13	AI12
A I 1 3	31	o 12	A114
A I 1 5	30	o 11	
A I 1 7	29	<u>。</u>	AI16
A119	28	• • 10	A I 1 8
A I 2 1	27	9	A120
•		8	A122
A I 2 3		o _ 7	A I 2 4
A I 2 5	25	> <u>6</u>	A126
A I 2 7	24	. •—	
A I 2 9	23	•	A128
A I 3 1	22	•	A I 3 0
	21	3	AGND
AGND		2	AGND
AGND	20	> _ 1	AGND
			1101112
		_	,

CN1 pins definition

Signal Name	Type	Function Definition
AI0~AI31	Input	AD analog signal input pins.
AGND	GND	Analog signal ground, input/output signal uses it as reference ground.

3.2 DI Definition of Signal Input and Output Connectors

20-core plug P1 pin definition

DI0	1	<u></u>	0-	2	DII
DI2	3	Ľ	٠ -	4	DI3
DI4	5	Γ	9	6	DI5
DI6	7	\Box	9	8	DI7
DI8	9	\mathbb{Z}	~	10	DI9
DI10	11	Ľ	<u>~</u>	12	DI11
DI12	13	\mathbb{Z}	9	14	DI13
DI14	15	\Box	9	16	DI15
DGND	17	Ľ	9	18	DGND
DGND	19	\mathbb{Z}	7	20	DGND
		껕	_		

P1 pins definition

Signal Name

DI0-DI15	Input	Digital signal input pins, DGND for reference ground.
DGND	GND	Digital signal ground

3.3 DO Definition of Signal Input and Output Connectors

20-core plug P2 pin definition

DO0	1	\Box	0-	2	DO1
DO2	3	Ľ	о О	4	DO3
DO4	5	Ľ	<u>-</u>	6	DO5
DO6	7	Ľ	~	8	DO7
DO8	9	Ľ	<u>-</u>	10	DO9
DO10	11	Ľ	^	12	DO11
DO12	13	Ľ	0	14	DO13
DO14	15	Ľ	~	16	DO15
DGND	17	Ľ	0	18	DGND
DGND	19	Ľ	5	20	DGND
		١,٠	_		

P2 pins definition

Signal Name	Type	Function Definition
DO0-DO15	Output	Digital signal input pins, DGND for reference ground.
DGND	GND	Digital signal ground

Chapter 4 Connection Ways for Each Signal

4.1 AD Single-ended Input Connection

Single-ended mode can achieve a signal input by one channel, and several signals use the common reference ground. This mode is widely applied in occasions of the small interference and relatively many channels.

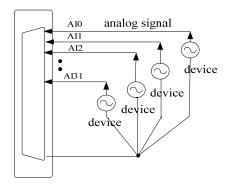


Figure 4.1 single-ended input connection

4.2 AD Double-ended Input Mode

Double-ended input mode, which was also called differential input mode, uses positive and negative channels to input a signal. This mode is mostly used when biggish interference happens and the channel numbers are few. Single-ended/double-ended mode can be set by the software, please refer to PCI8735 software manual.

According to the diagram below, PCI8735 board can be connected as analog voltage double-ended input mode, which can effectively suppress common-mode interference signal to improve the accuracy of acquisition. Positive side of the 16-channel analog input signal is connected to AI0~AI15, the negative side of the analog input signal is connected to AI16~AI31, equipments in industrial sites share the AGND with PCI8735 board.

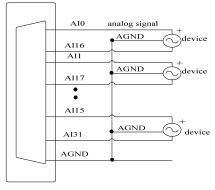


Figure 4.2 double-ended input connection

4.3 DIO Connection

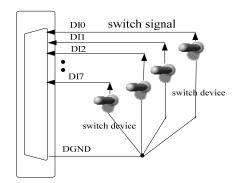


Figure 4.3 digital signal input connection

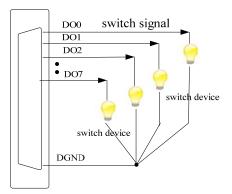


Figure 4.3 digital signal output connection

Chapter 5 Notes, Calibration and Warranty Policy

5.1 Notes

In our products' packing, user can find a user manual, a PCI8735 module and a quality guarantee card. Users must keep quality guarantee card carefully, if the products have some problems and need repairing, please send products together with quality guarantee card to ART, we will provide good after-sale service and solve the problem as quickly as we can.

When using PCI8735, in order to prevent the IC (chip) from electrostatic harm, please do not touch IC (chip) in the front panel of PCI8735 module.

5.2 AD Analog Signal Input Calibration

Every device has to be calibrated before sending from the factory. It is necessary to calibrate the module again if users want to after using for a period of time or changing the input range. PCI8735 default input range: ± 10 V, in the manual, we introduce how to calibrate PCI8735 in ± 10 V, calibrations of other input ranges are similar.

Prepare a digital voltage instrument which the resolution is more than 5.5 bit, install the PCI8735 module, and then power on, warm-up for fifteen minutes.

- Zero adjustment: select one channel of analog inputs, take the channel AI0 for example, connect 0V to AI0, and then run ART Data Acquisition Measurement Suite in the WINDOWS. Choose channel 0, ±10V input range and start sampling, adjust potentiometer RP1 in order to make voltage value is 0.000V or about 0.000V. Zero adjustment of other channels is alike.
- 2) Full-scale adjustment: select one channel of analog inputs, take the channel AI0 for example, connect 9997.55mV to AI0, and then run ART Data Acquisition Measurement Suite in the WINDOWS. Choose channel 0, ±10V input range and start sampling, adjust potentiometer RP2 in order to make voltage value is 9997.55mV or about 9997.55mV. Full-scale adjustment of other channels is alike.
- 3) Repeat steps above until meet the requirement.

5.3 Warranty Policy

Thank you for choosing ART. To understand your rights and enjoy all the after-sales services we offer, please read the following polices carefully.

- 1. Before using ART's products please read the user manual and follow the instructions exactly. When sending in damaged products for repair, please attach an RMA application form which can be downloaded from: www.art-control.com.
- 2. All ART products come with a limited two-year warranty:
- The warranty period starts on the day the product is shipped from ART's factory
- For products containing storage devices (hard drives, flash cards, etc.), please back up your data before sending them for repair. ART is not responsible for any loss of data.
- Please ensure the use of properly licensed software with our systems. ART does not condone the use of pirated software and will not service systems using such software. ART will not be held legally responsible for products

shipped with unlicensed software installed by the user.

- 3. Our repair service is not covered by ART's guarantee in the following situations:
- Damage caused by not following instructions in the User's Manual.
- Damage caused by carelessness on the user's part during product transportation.
- Damage caused by unsuitable storage environments (i.e. high temperatures, high humidity, or volatile chemicals).
- > Damage from improper repair by unauthorized ART technicians.
- Products with altered and/or damaged serial numbers are not entitled to our service.
- 4. Customers are responsible for shipping costs to transport damaged products to our company or sales office.
- 5. To ensure the speed and quality of product repair, please download an RMA application form from our company website.

Products Rapid Installation and Self-check

Rapid Installation

Product-driven procedure is the operating system adaptive installation mode. After inserting the disc, you can select the appropriate board type on the pop-up interface, click the button [driver installation]; or select CD-ROM drive in Resource Explorer, locate the product catalog and enter into the APP folder, and implement Setup.exe file. After the installation, pop-up CD-ROM, shut off your computer, insert the PCI card. If it is a USB product, it can be directly inserted into the device. When the system prompts that it finds a new hardware, you do not specify a drive path, the operating system can automatically look up it from the system directory, and then you can complete the installation.

Self-check

At this moment, there should be installation information of the installed device in the Device Manager (when the device does not work, you can check this item.). Open "Start -> Programs -> ART Demonstration Monitoring and Control System -> Corresponding Board -> Advanced Testing Presentation System", the program is a standard testing procedure. Based on the specification of Pin definition, connect the signal acquisition data and test whether AD is normal or not. Connect the input pins to the corresponding output pins and use the testing procedure to test whether the switch is normal or not.

Delete Wrong Installation

When you select the wrong drive, or viruses lead to driver error, you can carry out the following operations: In Resource Explorer, open CD-ROM drive, run Others-> SUPPORT-> PCI. bat procedures, and delete the hardware information that relevant to our boards, and then carry out the process of section I all over again, we can complete the new installation.